U.S. Department of mPTO 1449 Atty. Docket No. Serial No. nt of Commerce SUN-P5405 09/982,459 (Rev. 2-32) Patent and Trademark Office Applicant: Ralf M. Schmitt et al. Information Disclosure Statement by Applicant Filed: Group: 2825 October 17, 2001 (Use several sheets if necessary) **U.S. Patent Documents** Class Subclass Filing Date Document No. Date Name 7/1/97 Joly et al. 5,644,498 A 16 10/5/99 Aji et al. В 5,963,729 16 5 9/11/01 Yuan et al. C 6,289,412 703 14 7/7/98 Venkatesh 5'03 D 5,778,216 Hahn et al. 7/17/01 E 6,263,478 10 10/26/99 Li et al. F 5,974,245 LO Rusu et al. 12/3/96 G 5,581,473 ما 9/11/01 Dupenloup Η 6,289,498 18 Merryman 01/1999 I 5,864,487 06/1999 Naganuma J 5,917,729 10 04/2000 Shinagawa K 6,053,950 21 16 03/2001 Dupenloup L 6,205,572 5 716 07/2001 Mohan M 6,260,182 121 6,442,740 8/27/02 Kanamoto 716 1/26/99 Merryman 0 5,864,487 5,917,729 6/29/99 Naganuma Shinagawa <u>0</u> 6,053,950 4/25/00 6,205,572 3/20/01 Dupenloup 6,260,182 7/10/01 Mohan 6.442,740 8/27/02 Kanamoto **Foreign Documents** Translation Class Subclass Yes No Document No. Date Country 12/14/95 IJ WO 95/34036 wo Other Documents (Including Author, Title, Date, Pertinent Pages, etc.) R.B. Mueller et al., "Parallel Switch-Level Simulation for VLSI", IEEE, 1991, pp. 324-328 Scot Boon et al., "High Performance Clock Distribution for CMOS Asics", IEEE 1989, pp. 15.4.1 - 15.4.5 W H. Fair et al., "XP-000862228 SP 25.2: Clocking Design and Analysis for a 600MHz Alpha X Microprocessor", IEEE International Solid-State Circuits Conference, 1998, pp. 389-399 & 473 International Search Report, PCT/US 02/32937, International filing date October 15, 2002, date Search Report mailed January 28, 2004. Date Considered Examiner AURUST-200 Examiner: Interactive considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include a copy of this form with the next communication to applicant.